IN THE CLAIMS

Claims 1-20 (canceled).

21.   (currently amended)A disk array system, comprising:

a controller which controls writing of data to a plurality of storage regions and reading of data from said storage regions; and

a plurality of disk drives having said storage regions and having a plurality of first type disk drives each of which has a first type interface and a plurality of second type disk drives each of which has a second type interface,

wherein said controller controls whether an examination of data stored in said disk drives is performed based on whether data to be examined is stored in said second type disk drives in which the examination is performed or in said first type disk drives in which the examination is not performed, and

wherein said examination is-performed by said controller is a process for indicating whether data stored in said second type disk drives is in a false state or not.

22.   (currently amended)A disk array system according to claim 21, wherein data in said false state are data which are in an address specified for new data by said controller and are not written by said new data, and

wherein said new data are written to another address which is different from said address specified for said new data.

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23.   (currently amended)A disk array system according to claim 21, wherein data in said false state are data of another contents stored in said second type disk drives, said another contents are different from contents specified by said controller.

24.   (currently amended)A disk array system according to claim 21, wherein said examination is one or more of four examinations including:

a first examination which is to compare first contents corresponding to data written to a first region in said second type disk drives and data read from said first region, said first contents being stored in said controller until said comparison at the earliest,

a second examination which is to examine a first parity check by using a plurality of first data and a first parity data of said plurality of first data read from some of said second type disk drives,

a third examination which is to examine a second parity check by using one of said plurality of first data, another data and a second parity data read from one or more of said second type disk drives, said second parity data corresponding to said one of said plurality of first data and said another data, and

a fourth examination which is to examine whether a head in one of said second type disk drives is normal or abnormal by comparing second contents corresponding to data written to predetermined second region in said one of said second type disk drives and data read from said predetermined second region, said second contents being stored in said controller until said comparing at the earliest, and

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wherein said first through fourth examinations are controlled by said

controller.

25.   (currently amended)A disk array system according to claim 21, further

comprising:

another controller controlling to write data to said storage regions and to read

data from said storage regions;

a first data line coupled to said controller and said disk drives; and

a second data line coupled to said another controller and said disk drives.

26.   (currently amended)A disk array system according to claim 21, further

comprising:

a first housing having said first type disk drives; and

a second housing having said second type disk drives.

27.   (currently amended)A disk array system according to claim 21, further

comprising:

another controller controlling to write data to said storage regions and to read

data from said storage regions;

a first data line coupled to said controller and said disk drives;

a second data line coupled to said another controller and said disk drives;

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a first housing having some of said disk drives and coupled by said first data

line and said second data line to a second housing which have the other of said disk

drives;

a plurality of circuits, in said first housing, having a first circuit coupled to said

first data line and a second circuit coupled to said second data line and controlling to

establish communication between said controller and said second housing and to

separate said first housing and transferring data between said controller and said

second housing if said first housing has a failure;

a plurality of converters, in said first housing, coupled to said circuits

converting a first type interface and a second type interface; and

a plurality of switches, in said first housing, each coupled to said converters

converts-and said some of disk drives and communicating to a selected converter of

said converters,

wherein said first type interface is used using-in said first data line and said

second data line.

28.   (currently amended)A disk array system according to claim 21, further

comprising:

another controller controlling to write data to said storage regions and to read

data from said storage regions;

a first data line coupled to said controller and said disk drives;

a second data line coupled to said another controller and said disk drives; and

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a plurality of converters coupled to said first data line and said second data

line and converting a first type interface and a second type interface and

communicating to a selected data line among said first line lien and said second data

line,

wherein said first type interface is used using in said first data line and said

second data line.

29.   (currently amended)A disk array system according to claim 21, wherein

another controller controlling to write data to said storage regions and to read data

from said storage regions; .

wherein a first data line is coupled to said controller and said disk drives;

wherein a second data line is coupled to said another controller and said disk

drives;

wherein said first data line is a Fibre Channel Arbitrated Loop (FC-AL);

wherein said second data line is a FC-AL;

wherein said first type interface is a fibre channel interface, and

wherein said second type interface is an Advanced Technology (AT)

Attachment (ATA) interface.

30.   (currently amended)A disk array system according to claim 21, further

comprising:

a first housing having said first type disk drives; and

a second housing having said second type disk drives,

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wherein said first housing is between said controller and said second housing,

wherein said first type interface is a fibre channel interface, and

wherein said second type interface is a Advanced Technology (AT) Attachment (ATA) interface.

31.   (currently amended)A disk array system according to claim 21, wherein said first type interface is a fibre channel interface,

wherein said second type interface is an ATA interface,

wherein some of said first type disk drives comprise are comprising a Redundant Array of Independent Disks (RAID) group, and

wherein some of said second type disk drives comprise are comprising another RAID group.

32.   (currently amended)A disk array system according to claim 21, wherein said second type interface is an ATA interface, and

wherein some of said second type disk drives are used to store data, said the data is are used to back up.

33.   (new) A disk array system, comprising:

a controller which controls writing of data to a plurality of storage regions and reading of data from said storage regions; and

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a plurality of disk drives having said storage regions and having a plurality of

first type disk drives each of which has a first type interface and a plurality of second

type disk drives each of which has a second type interface,

wherein said controller controls whether an examination of data stored in said

disk drives is performed based on whether data to be examined is stored in said

second type disk drives, and

wherein said examination performed by said controller is a process for

indicating whether data stored in said second type disk drives is in a false state or

not.

34.   (new) A disk array system according to claim 33, wherein said first

type interface is a Fibre Channel (FC) interface and said second type interface is a

Serial ATA (SATA) interface.

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